



SPECIFICATION AMENDMENTS

Please amend the specification on page 5, line 3 as follows:

referred to as satellite or satellites 40. The satellites 40 preferably contain an on-board processor (OBP) 42 and an on-board memory (MEM) 43. An Inter-Satellite Link ~~(ISL) 44~~ (ISL) 41 is shown between satellites 40A, 40B and 40C. The ISL 41 could be implemented using an RF link or an optical link, and is modulated with information that is transferred between the satellites 40, as described in further detail below. More than three satellites 40 can be coupled together using ISLs 41.

Please replace the current Abstract with the following Abstract: